

JC986 U.S. PTO
09/973002
10/10/01

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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PATENT NUMBER

U.S. **UTILITY** Patent Application

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| O.I.P.E. | PATENT DATE |
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| APPLICATION NO. 09/973002 | CONT/PRIOR F | CLASS 438 257 | SUBCLASS 684 | ART UNIT 2842 | EXAMINER <i>Edw. J. ...</i> |
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| TITLE | APPLICANTS |
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| 95. Member | 95. Mr. J. |

Tetsuya Hiraoaka
Akira Takashima

28257 Cmg

Semiconductor device with stack of semiconductor chips

PTO-2040
12/99[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
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